IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: MING-DOU KER ET AL.   )
)
Application Serial No.: 10/810,645   )   Attorney Docket No.: IT0009-US
)
Filed: March 29, 2004   )
)
Title: AUTOMATIC TRANSMISSION   )
LINE PULSE SYSTEM   )

SUBMISSION OF REVOCATION OF POWER OF ATTORNEY AND GRANT OF POWER OF ATTORNEY

Assistant Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants hereby submit the attached Revocation of Power of Attorney and Grant of Power of Attorney in the above-identified application. Should there be any questions with respect to this submission a representative of the Patent Office is requested to contact the undersigned.

Respectfully submitted,

MING-DOU KER ET AL.

Date: August 6, 2004   By: _____
Lawrence D. Eisen

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Patent Application and Patent Numbers: See attached "Schedule A"

REVOCATION OF POWER OF ATTORNEY
AND GRANT OF NEW POWER OF ATTORNEY

The undersigned, a representative authorized to sign on behalf of the assignee owning all of the interest in the listed and pending patent applications and issued patents on the attached sheet (Schedule A), hereby revokes all previous powers of attorney or authorization of agent granted in these patents before the date of execution hereof. The undersigned verifies that **INDUSTRIAL TECHNOLOGY RESEARCH INSTITUTE** is the assignee of the entire right, title, and interest in each of the pending patent applications and issued patents listed on the attached Schedule A and is identified as the assignee by assignments from the inventor(s) in the listed pending patent applications and issued patents as filed accordingly at the U.S. Patent and Trademark Office. The undersigned certifies that the evidentiary documents have been reviewed and to the best of the undersigned's knowledge and belief, title in each of the pending patent applications and issued patents listed on the attached Schedule A is in the assignee **INDUSTRIAL TECHNOLOGY RESEARCH INSTITUTE.**

By the undersigned's signature, **INDUSTRIAL TECHNOLOGY RESEARCH INSTITUTE** hereby grants power of attorney for each of the pending patent applications and issued patents listed on the attached Schedule A to **SHAW PITTMAN LLP**, Michael D. Bednarek, Reg. No. 32,329; Lawrence J. Gotts, Reg. No. 31,163; Aslan Baghdadi, Reg. No. 34,542; Yitai Hu, Reg. No. 40,653; Elizabeth M. Roesel, Reg. No. 34,878; David C. Isaacson, Reg. No. 38,500; Steven P. Arnheim, Reg. No. 43, 475; Poh C. Chua, Reg. No. 44,615; Michael A. Oblon, Reg. No. 42,956; Lawrence D. Eisen, Reg. No. 41,009; Mark Koehn, Reg. No. 46,271; Michelle S. Marks, Reg. No. 41,971; Brett C. Martin, Reg. No. 52,000; Chad D. Wells, Reg. No. 50,875; Tara L. Hutchings, Reg. No. 46,559; John Kasha, Reg. No. 53,100; Ann P. McGeehan, Reg. No. 45,839; June E. Cohan, Reg. No. 43,741; and Joanne Kim, Reg. No. 51,193, both jointly and separately as its attorneys with full power of substitution and revocation, to transact all business in the Patent and Trademark Office connected with each of the pending patent applications and issued patents listed on the attached Schedule A.

Please send all future correspondence concerning the pending patent applications and issued patents listed on the attached Schedule A to SHAW PITTMAN LLP at the following address:

Shaw Pittman LLP
1650 Tysons Blvd.
McLean, Virginia 22102

Dated: 7/9/2004   By: _Alex Fan_

Alex Fan
Director
IP Management & Legal Affairs Div./TTC
Industrial Technology Research Institute

REVOCATION OF POWER OF ATTORNEY
AND GRANT OF NEW POWER OF ATTORNEY

U.S. PATENT APPLICATIONS:

Application Number	Former Docket No.	Current Docket No.
As Attach file (total 29 files)		

U.S. PATENTS:

Patent Number	Former Docket No.	Current Docket No.

Case No.	Title	Filing Date	Appln. No.
IT0007-PRO	Turn-On-Efficient Bipolar Structures With Deep N-Well for On-Chip ESD Protection Design	7/17/03	60/487,581
IT0007-US	Turn-On-Efficient Bipolar Structures With Deep N-Well for On-Chip ESD Protection Design	12/5/03	10/727,550
IT0008-US	ESD Protection Design against Charge-Device Model ESD Events	12/4/03	10/726,641
IT0009-US	Automatic Transmission Line Pulse System	3/29/04	10/810,645
IT0012-US	Electrostatic Discharge Protection Device and Method Using Depletion Switch	12/3/02	10/307,969
IT0013-US	Perturbation Apparatus and Method for User Detection in a Multiple-Access Communication System	7/12/01	09/902,695
IT0014-US	System and Method for Providing Voice Communication for Radio	8/7/01	09/922,750
IT0015-US	Closed-Loop Power Control Method for a Code-Division Multiple-Access Cellular System	9/21/01	09/956,785
IT0016-US	Speech Recognition Involving a Neural Network	6/13/02	10/167,589
IT0017-US	Software Defined Radio (SDR) Architecture for Wireless Digital Communication Systems	2/14/02	10/073,933
IT0018-PRO	Input Stage for Mixed-Voltage-Tolerant Buffer Without Leakage Issue	11/20/03	60/523,317
IT0018-US	Input Stage for Mixed-Voltage-Tolerant Buffer Without Leakage Issue	6/21/04	10/871,348
IT0019-US	ESD Protection Circuits for Mixed-Voltage Buffers	5/2/03	10/428,047
IT0020-US	Silicon-Controlled Rectifier With Dynamic Holding Voltage for On-Chip Electrostatic Discharge Protection	3/28/03	10/400,874
IT0020-CIP	Silicon-Controlled Rectifier With Dynamic Holding Voltage for On-Chip Electrostatic Discharge Protection	12/4/03	10/726,490
IT0021-US	System and Method for Partitioning Streaming Data Coded with Fine Granularity Scalability for use in a Cable Network	11/7/02	10/289,426
IT0022-US	Charge-Device Model Electrostatic Discharge Protection Using Active Devices for CMOS Circuits	5/21/03	10/442,261
IT0023-US	Enhanced Wireless Communication System and Method Thereof	7/15/03	10/618,632
IT0024-PRO	FGS Over SCDMA By Steam-Code Partition	5/17/02	60/380,861
IT0025-PRO	Method and System of Interference Cancellation In Multi-Cell CDMA Systems	3/19/02	60/365,181
IT0026-PRO	New Interleaver Design and Associated Encoding and Decoding Structures for Turbo Codes	8/27/01	60/314,673
	Polysilicon SCR devices in CMOS Process and Their Applications for on Chip ESD Protection		
	Encoding and Decoding Apparatus and Method	2/6/02	10/066,658
	Method and System of Interference Cancellation in Multi-Cell CDMA Systems	8/15/02	10/218,456
	Mixed-Voltage CMOS I/O Buffer with Thin Oxide Device and Dynamic N-Well Bias Circuit	4/6/04	93109440